

# Radiofrequency planar surface coil for Magnetic Resonance: when the use of a circular wire gives a noticeable advantage with respect to a flat strip conductor?

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**Abbreviated title:** Circular wire and flat strip conductors for Magnetic Resonance coils

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## **Abstract**

Depending on their cross-sectional shape, commonly used conductors for radiofrequency (RF) Magnetic Resonance (MR) coils can be categorized into circular wires and flat strips. Due to a more symmetrical current distribution inside conductor volume, coils constituted by wire conductors provide better overall performance in unloaded conditions with respect to the ones made of strip conductor, although wire conductors are difficult to handle for coil manufacturing and additional mechanical competencies are required. Nevertheless, the accomplishment of the best coil performance during imaging, i.e. in the presence of a sample, remains the main issue in MRI. It follows that the use of wire conductors instead of strip ones is worthwhile only if the correspondent increase in coil quality factor with sample is substantial: this is related to the ratio between sample and coil resistance.

This paper proposes the application of a finite element method (FEM)-based numerical approach for separately estimating the conductor and radiative losses in planar surface loops characterized by different cross-sectional shapes (circular wire and flat strip) in conjunction with a vector potential calculation-based method for sample-induced resistance estimation. Simulation data were acquired from 5.7 to 128 MHz, for four different size loops (from 2 to 15 cm diameters), with the scope of evaluating the region in the frequency-loop diameter plane where the use of a circular wire conductor gives a noticeable advantage with respect to the flat strip in maximizing signal-to-noise ratio (SNR) in MR applications.

**Keywords:** numerical methods; radiofrequency coils; coil losses; sample losses; signal-to-noise ratio; Magnetic Resonance

## **1. Introduction**

Signal-to-noise ratio (SNR) in magnetic resonance (MR) experiments is affected by the coil resistance and by the sample-induced resistance values [1]. In particular, at lower frequencies SNR

is mainly ascribed to the coil losses [2] whereas the sample losses are dominant [3] at higher frequencies, as it generally happens for 1.5 T and 3 T  $^1\text{H}$  coils design.

Coil resistance strongly depends on conductor cross-sectional shape; this latter can be classified as circular rod shapes (also referred to as *circular wires*) and rectangular shapes (*flat strips*) [4]. Moreover, the radiofrequency (RF) energy loss is influenced by radiation resistance, which takes into account for the *antenna effect* [5].

In a previous work [6], authors proposed the use of finite element method (FEM)-based numerical approach for separately estimating the conductor and radiative losses in circular wire loop RF coils for different tuning frequencies (from 5.7 to 128 MHz). In a successive work [7], the same simulation approach has been employed for flat strip loops and simulation results were validated through workbench tests performed on flat strip and circular wire coil prototypes.

However such approach, since sample losses are neglected, is mainly useful when an optimal coil design is pursued by minimizing the coil noise, and it does not consider the sample noise; indeed, this last can be dominant when the sample dimensions are not small compared to the wavelength, i.e., at high field MR imaging [8].

As an example, we mention the loop-coils for MR micro-imaging, where wires are used since, in this case, the sample losses are small compared to coil ones. Moving towards clinical applications, loop coils may have larger diameters, increased sample volume and resistance, and solutions using flat strip loops can be found in the market. The use of flat strips instead of circular wire for coil loops is advantageous only if the coil quality factor increase in imaging conditions, i.e. in the presence of the sample.

Sample-induced resistance can be estimated by using a method which employs vector potential calculation (VPC), which can be easily implemented in analytical form for sample-induced resistance estimation of simple coil and sample geometries [9].

In this paper we propose the application of FEM for estimating flat strip and circular wire loop coil losses and VPC for sample-induced resistances estimation from 0.13 T to 3.0 T, i.e. from 5.7 MHz

to 128 MHz, for four different loop sizes (from 2 to 15 cm diameters), with the aim of evaluating the combination of frequency and loop diameter where the use of a circular wire gives a noticeable advantage with respect to the flat strip.

## 2. Materials and Methods

### 2.1 RLC circuit and SNR estimation

RF coil study can be approached by using an equivalent *RLC* circuit (Fig. 1), in which the current  $I$  is generated by  $V$ , alternately the voltage source (for transmitter coil) or the sample-induced voltage (for receiver coil), as according to the reciprocity theorem [10].  $L$  is the system inductance which depends on conductors size and geometry and takes into account the energy stored in the magnetic field, while  $C$  is the system capacitance resulting from the contribution of discrete capacitors.

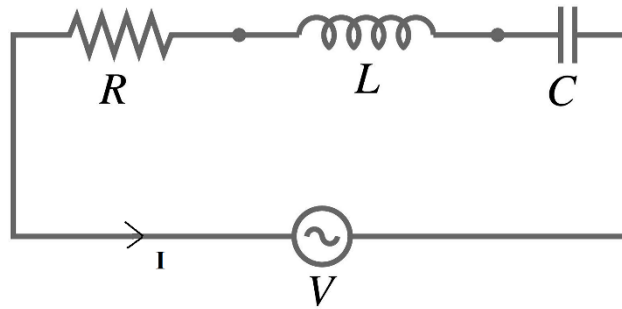


Fig. 1

Both  $L$  and  $C$  affect the circuit resonant frequency, which can be calculated as:

$$f_0 = \frac{1}{2\pi\sqrt{LC}} \quad (1)$$

The resistance  $R$  is the overall sum of the resistances associated with the loss mechanisms within the conductor and within the sample [11] and can be evaluated as:

$$R = R_{coil} + R_{sample} + R_{extra} \quad (2)$$

$R_{coil}$  takes into account the losses within the coil conductors and depends on the conductor geometry,  $R_{sample}$  represents the sample losses caused by RF currents, induced by the fluctuating

magnetic field and by electric fields in the sample. Finally, the term  $R_{extra}$  indicates radiative losses ( $R_{irr}$ ), tuning capacitor losses ( $R_{cap}$ ) and soldering losses ( $R_{sold}$ ).

A quantitative measure of coil quality factor  $Q$  can be expressed in terms of circuit parameters as [8]:

$$Q = \frac{2\pi f_0 L}{R} = \frac{1}{R} \sqrt{\frac{L}{C}} \quad (3)$$

while the sensitivity to loading can be evaluated through the ratio  $r$  between the unloaded quality factor ( $Q_{empty}$ ) and the one in loaded condition ( $Q_{sample}$ ) [12]:

$$r = \frac{Q_{empty}}{Q_{sample}} = 1 + \frac{R_{sample}}{R_{coil} + R_{extra}} \quad (4)$$

Generally, a coil has to be designed by minimizing the coil noise with respect to the sample noise,

thus allowing to achieve the highest SNR from a given coil architecture, being  $SNR \approx \sqrt{1 - \frac{1}{r}}$  [13],

which can be written in terms of loss contributions by using Eq. (4) as

$$SNR \approx \sqrt{\frac{R_{sample}}{R_{sample} + R_{coil} + R_{extra}}}$$

## 2.2 Coil losses estimation by FEM

Coil losses prediction for circular wire and flat strip conductors can be achieved using a numerical method implementing the FEM algorithm, such as HFSS (Ansys, Canonsburg, PA, USA) employed in this work. A set of simulations has been performed investigating circular coils with different size (2, 5, 10 and 15 cm diameters) constituted by circular wire and flat strip conductors, whose sizes guarantee the same inductance between strip and wire conductors (0.1 cm radius circular wire and 4.482 mm width  $\times$  0.04 mm thickness strip) [4]. All coils were simulated as purely inductive, without tuning capacitors, in order to reduce the computation time [14], and the impedances were calculated at different frequencies ( $f=5.7, 21.3, 63.9$  and  $127.8$  MHz). Lumped port (S-port), employed as feeding source, allows calculating the S parameters of the electromagnetic model

based on a defined reference impedance (50  $\Omega$ ) and also allows to calculate the corresponding input impedance.

The estimated real part of the impedance corresponds to  $R$ , constituted by the sum of coil resistance ( $R_{coil}$ ) and radiative losses ( $R_{irr}$ ), which can be evaluated separately with FEM. As in the previously cited works [6, 7], the simulations have been performed using an adaptive tetrahedral mesh with an automatic convergence detection. The mesh adaptation procedure provided a minimum edge length of 9.8  $\mu\text{m}$  and 8  $\mu\text{m}$  for the circular wire and the flat strip coil, respectively.

### ***2.3 Sample-induced resistance calculation with VPC***

The estimation of losses associated to sample-induced resistance has been performed by using homogeneous infinitely long cylinders [15, 16], spheres [1] or half-spaces [17] as approximations of the sample geometry.

In this work, a VPC method based on the magnetostatic approach has been used. Depending on the coil geometry and shape, sample-induced resistance estimation can be performed by using [18]:

$$R_{sample} = \sigma \omega^2 \int_{vol} A * A dV \quad (5)$$

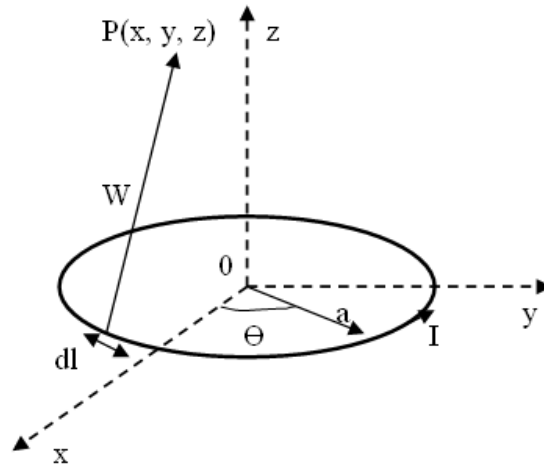
where  $\sigma$  is the sample conductivity,  $A$  is the magnetic vector potential produced by the current flowing in the coil and the integral is performed over the sample volume.

Different algorithms can be used in Eq. (5) for vector potential calculation, assessed with Legendre polynomials [19], spherical harmonics [18], surface integrals [20] and Lipschitz-Hankel integral [21]. However, all these methods are known to require convoluted calculations, whereas good accuracy can be achieved even with a simple method, which can be easily mathematically implemented.

The magnetic vector potential produced by a current flowing in a loop can be computed as [8]:

$$A(r) = \frac{\mu_0 I}{4\pi} \int_{C_c} \frac{dl}{W} \quad (6)$$

where  $I$  is the electric current flowing in the  $C$  contour,  $dl$  is the infinitesimal vector tangential to  $C$  and  $W$  is the distance between the observation point with  $(x, y, z)$  coordinates and the conductor path (see Fig. 2). Eq. (6) can be easily solved through Elliptic integrals [22].



**Fig. 2**

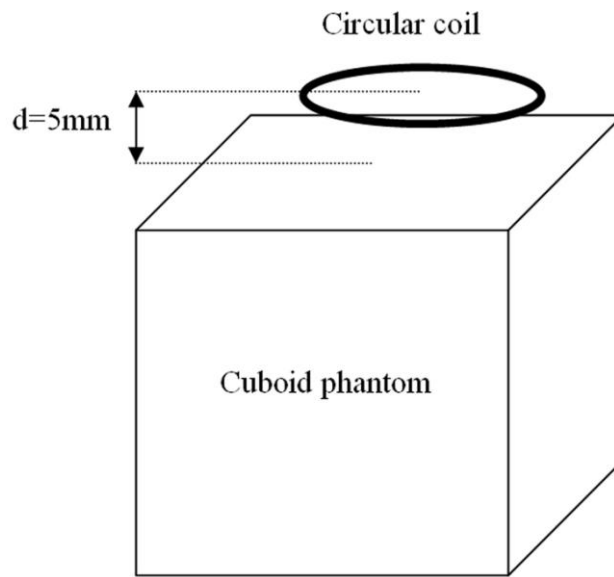
#### **2.4 Capacitance and soldering losses**

It is well known that capacitor losses can reduce coil overall performance, especially for lower frequency designs. Therefore in order to obtain high-performance coils, the use of high-quality capacitors is well recommended. For achieving an estimation of capacitor losses, the inductance values of the four different size loops were calculated by using an in-house software simulator [23]; the capacitance values have been therefore calculated for tuning the coils at the four different frequencies. Finally, capacitance losses can be estimated from capacitor datasheets [24] by using the dependence of capacitor equivalent series resistance (ESR) with the frequency according to the  $f^{1/2}$  law, which is the same as for the conductor skin depth losses, being ESR largely determined by the capacitor electrodes resistance [25].

Further resistive losses can be ascribed to the solder joints connecting the coil components, which can be extrapolated from literature data [26, 23].

### 3. Results

Coil resistances have been computed by FEM simulations while sample-induced resistances have been estimated by using Eq. (5) and integrating in a  $30 \times 30 \times 30 \text{ cm}^3$  cuboid whose electrical conductivity ( $\sigma=0.39 \text{ S/m}$ ) meets the American Society for Testing and Material (ASTM) criteria for MR phantom developing [27]. The distances between the loops plane and the phantoms are equal to 5 mm for all simulations. Fig. 3 depicts the simulation setup showing the coil geometry and the simulated phantom.



**Fig. 3**

Table 1, 2, 3 and 4 show different size circular wire and flat strip coil simulation results at different frequencies.

FEM simulations confirmed the higher value of losses of the coils constituted by flat strip conductor with respect to the circular wire one at all frequencies and for all coil sizes, while  $R_{irr}$  was approximately the same for flat strip and circular wire coils.

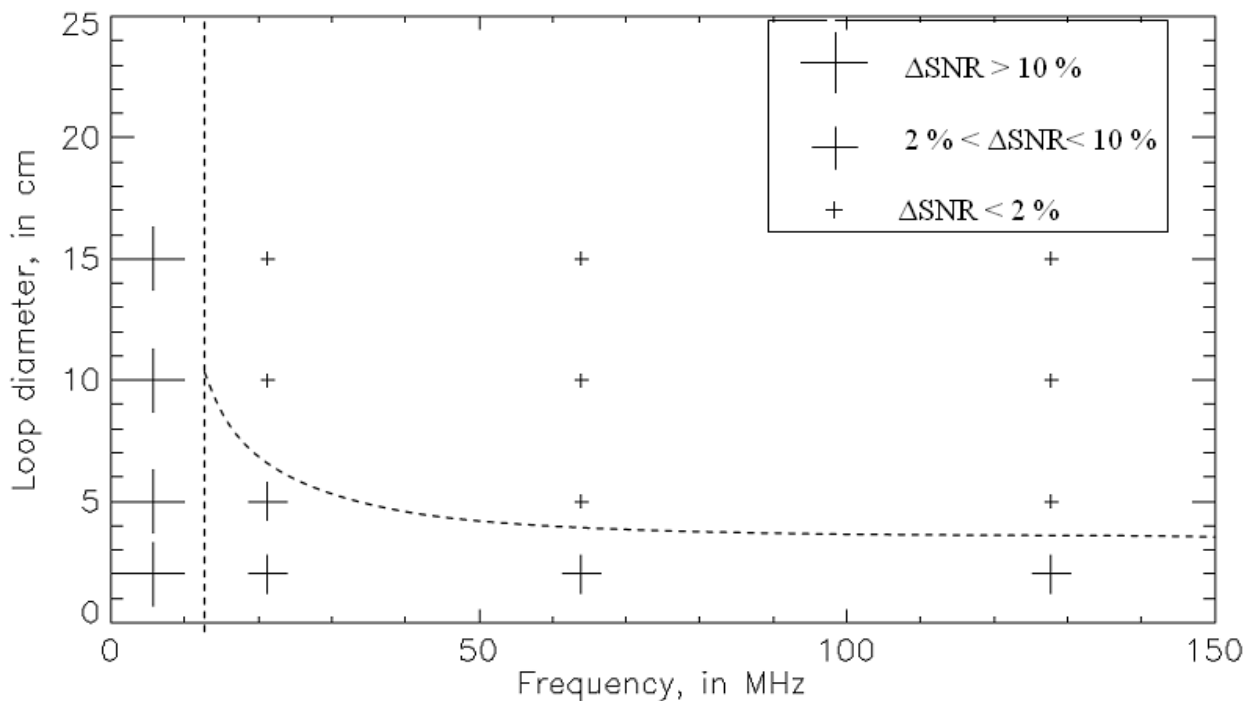
The term  $\Delta SNR$  indicates the gain in SNR when using circular wire instead of flat strip, calculated as  $\Delta SNR = (SNR_{wire} - SNR_{strip}) / SNR_{strip}$ , being SNR estimated according to the previously



described theoretical formulation [13] and by using Eq. (4). For strip and wire coils of the same diameter,  $\Delta\text{SNR}$  increase when the coils are tuned at lower frequencies, because the SNR becomes strongly dependent on the coil losses and an optimal coil design is a necessary constraint for minimizing the coil noise with respect to the sample noise.

When coil diameter diminishes,  $\Delta\text{SNR}$  increases for each tuning frequency. Only for the smaller 2 cm coil the trend of the increasing  $\Delta\text{SNR}$  persists only for the higher frequencies, whereas it decreases for the lower one: as a matter of fact, in the regime dominated by the coil losses, these losses begin to be comparable with those in the capacitors and soldering.

Fig. 4 summarizes the previous data by presenting them in three different regions of  $\Delta\text{SNR}$  value ranges in dependence on loop diameter and frequency.



**Fig. 4**

#### 4. Discussion and conclusion

This paper proposed the application of a numerical method for estimating coil losses jointly to a vector potential calculation-based method for power losses estimation in the sample.

In particular, a FEM-based numerical approach has been employed for separately estimating the conductor and radiative losses in planar surface loops characterized by different cross-sectional shapes (circular wire and flat strip). The sample-induced resistance was estimated with a different method, easily implemented in a code, because FEM would have requested an unacceptable computational time for simulating loaded coils with respect to the time guaranteed by VPC, especially for low-frequency tuned coils. Moreover, VPC approach can be properly performed when the simulated coil has a very simple geometry (e.g. circular, square or solenoidal) and the sample can be approximated by a simplified model.

The vector potential is evaluated by using quasi-static assumption, that implies a nearly static magnetic field assumption which is valid when the coil dimensions are much lower than the wavelength. This assumption is verified even at higher frequencies used in the described applications (at 128 MHz the related wavelength is 2.35 m and the nearly static assumption holds for any loop dimensions). However, its use would result in a significant error when the coil and the sample sizes are comparable to the wavelength. Both simulators have been previously validated with home-built circular coils: in particular, workbench tests for coil resistance measurements performed on two circular coil prototypes, the first one constituted by a flat strip and the second one by circular wire conductors, showed a good agreement with FEM simulations [6, 7], whereas sample-induced resistance estimation method has been validated by comparison between simulation and workbench test performed on different size coils tuned at different frequencies and loaded with a cubic sample [9].

In this work, two main issues have been addressed: 1) derive the conditions for which the sample noise dominates or is negligible with respect to the coil losses; 2) find the combination of frequencies and loop diameters where the use of a circular wire gives a noticeable advantage with respect to a flat strip. As expected, the results highlight the better performance of the coil constituted by a wire conductor with respect to the strip one in all frequency range routinely used in MR clinical scanner (5.7–128 MHz). Since a wire conductor is difficult to handle for coil

manufacturing and requires qualified mechanical personnel, for achieving a significant SNR gain we suggest using it especially when the frequency is lower or equal to 5.7 MHz for all coil diameters or for frequencies below 63.9 MHz when the loop diameter is smaller or equal to 5 cm.

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**Fig. 1** RLC equivalent circuit of a radiofrequency coil

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**Fig. 4** SNR gain as a function of loop diameter and frequency

## Tables

Frequency (MHz)	R <sub>coil-strip</sub> (mΩ)	R <sub>coil-wire</sub> (mΩ)	R <sub>irr-strip</sub> (mΩ)	R <sub>irr-wire</sub> (mΩ)	R <sub>cap</sub> (mΩ)	R <sub>sold</sub> (mΩ)	R <sub>sample</sub> (mΩ)	ΔSNR (%)
5.7	10.63	6.32	0	0	3	5	0.09473	14.0
21.3	13.77	12.27	0	0	6	10	1.32278	2.5
63.9	25.88	21.33	0.0061	0.0059	20	25	12.2859	2.9
127.8	38.04	30.33	0.0997	0.0977	40	60	49.1438	2.1

**Tab. 1** 2 cm diameter loop

Frequency (MHz)	R <sub>coil-strip</sub> (mΩ)	R <sub>coil-wire</sub> (mΩ)	R <sub>irr-strip</sub> (mΩ)	R <sub>irr-wire</sub> (mΩ)	R <sub>cap</sub> (mΩ)	R <sub>sold</sub> (mΩ)	R <sub>sample</sub> (Ω)	ΔSNR (%)
5.7	28.33	15.73	0	0	3	5	0.002214	21.9
21.3	36.26	30.54	0.0029	0.0030	12	10	0.03092	3.4
63.9	68.16	53.45	0.2409	0.2500	30	25	0.28718	1.8
127.8	101.96	77.68	4.1044	4.2616	40	60	1.1487	0.9

**Tab. 2** 5 cm diameter loop

Frequency (MHz)	R <sub>coil-strip</sub> (mΩ)	R <sub>coil-wire</sub> (mΩ)	R <sub>irr-strip</sub> (mΩ)	R <sub>irr-wire</sub> (mΩ)	R <sub>cap</sub> (mΩ)	R <sub>sold</sub> (mΩ)	R <sub>sample</sub> (Ω)	ΔSNR (%)
5.7	56.99	31.47	0	0	3	5	0.018977	19.9
21.3	72.92	61.31	0.0468	0.0491	12	10	0.265	1.7
63.9	140.04	109.99	4.0147	4.2093	30	25	2.4611	0.6
127.8	226.33	172.64	77.57	81.285	40	60	9.8443	0.3

**Tab. 3** 10 cm diameter loop

Frequency (MHz)	R <sub>coil-strip</sub> (mΩ)	R <sub>coil-wire</sub> (mΩ)	R <sub>irr-strip</sub> (mΩ)	R <sub>irr-wire</sub> (mΩ)	R <sub>cap</sub> (mΩ)	R <sub>sold</sub> (mΩ)	R <sub>sample</sub> (Ω)	ΔSNR (%)
5.7	96.55	47.14	0.0013	0.0013	3	5	0.059145	19.7
21.3	125.17	92.49	0.25	0.25	12	10	0.8259	1.7
63.9	253.56	173.39	23.05	22.91	30	25	7.6707	0.5
127.8	485.43	310.80	570.10	561.03	40	60	30.6828	0.3

**Tab. 4** 15 cm diameter loop

**Supplementary Material**

[Click here to download Supplementary Material: Sample-coil-rev-marked.doc](#)